

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Masaki ISHIDAO et al.

Group Art Unit: 2818

Serial Number: 10/768,188

Examiner: Long K. Tran

Filed: February 2, 2004

Confirmation Number: 3660

SEMICONDUCTOR MEMORY DEVICE AND MANUFACTURING

METHOD THEREOF

Attorney Docket Number: 032209

Customer Number: 38834

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450 Date: July 13, 2005

Sir:

This paper is submitted in response to the Official Action dated June 17, 2005.

In the Action, restriction is required between Group I, Claims 1-9 and Group II, Claims 10-20.

Applicants hereby elect the subject matter of Group I, Claims 1-9 for prosecution in this application. This election is made without traverse, and it is understood that Applicants' rights to the filing of a divisional application directed to the non-elected subject matter under 35 U.S.C. §120 and 35 U.S.C. §121 are retained.

Response to Restriction Requirement Serial No. 10/768,188 Attorney Docket No. 032209

If this paper is not timely filed, Applicant(s) respectfully petition(s) for an appropriate extension of time. The fees for such an extension or any other fees that may be due with respect to this paper may be charged to Deposit Account No. 50-2866.

Respectfully submitted,

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